REMARKS

This preliminary amendment is submitted concurrently with the above-referenced continuation patent application. Applicants are filing this continuation application to prosecute claims covering a method and circuit for operating an image processing system with an analog front end circuit having an analog-to-digital converter (ADC) in a reduced resolution mode. These claims have not been previously presented and have a different scope than those claims prosecuted in the parent case.

Applicant has inserted a paragraph in this continuation application, which claims the benefit of the parent case. Applicant has also amended the paragraph starting at page 1, line 6 to include the respective serial numbers. Applicant has further amended this paragraph to include other information such as the issued U.S. patent numbers, the issue dates, and statuses for the respective patent cases.

No other fees are believed to be due by the filing of this Preliminary Amendment; however, if any other fees are due by the filing of the enclosed documents, including any fees incurred by an extension of term, please consider this paragraph such a Request for term, and charge any fees associated with the Request or any other fees incurred by the filing of this document to Cirrus Logic Deposit Account No. 03-2028/0929-CS-C1.

Respectfully submitted,

Date: Apr. 18, 2004

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